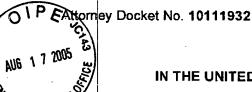
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PATENT



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Applicant:

Tie Jiang WU, Chien-Chang

Filed:

3/26/2004

HUANG, Yu-Wei TING, Bo Ching

JIANG, Chin-Ling HUANG

Appl. No.: 10/809,999 Examiner:

Doan, Theresa T

Conf. No.: 8487

Art Unit: 2814

-iTitle:

METHOD AND DEVICE FOR DETECTING WHETHER THE ALIGNMENT

OF BIT LINE CONTACTS AND ACTIVE AREAS IN DRAM DEVICES IS

NORMAL

Date: August 17, 2005

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FEE(S) TRANSMITTAL

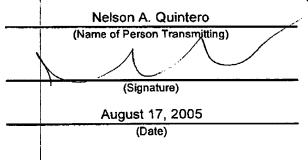
Attached please find:

- 1) Form PTOL-85 Part B Fee(s) Transmittal (1 page); and
- 2) Form PTO-2038 (1 page).

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Respectfully submitted,

Nelson A. Quintero Attorney for Applicant Reg. No. 52,143 Customer No. 34,283

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